

**SPECIFICATION**

Please amend the Specification as follows:

Please replace the first full paragraph on Page 10 with the following:

Thus there are two possibilities for an invalidate operation. If the EA to RA mapping is valid for some but not all threads, then a selective invalidate is done, and the appropriate thread valid indicators for ERAT table entry 108 are set to 0 for those threads for which the mapping is invalid. If the EA to RA mapping is invalid for all threads, then ERAT table entry 108 is invalidated by setting all the thread valid indicators to 0.

Please replace the first full paragraph on Page 14 with the following:

In ~~Fig.~~ FIG. 2, a dual-thread MPU is used as an example. It should be noted however, that as previously discussed, the present invention can be extended to support more than two threads.